

LOW-VOLTAGE 10-BIT BUS SWITCH WITH ACTIVE HIGH AND LOW ENABLES

IDT74CBTLV3862

FEATURES:

- 5Ω A/B bi-directional switch
- Isolation Under Power-Off Conditions
- · Over-voltage tolerant
- · Latch-up performance exceeds 100mA
- Vcc = 2.3V 3.6V, normal range
- ESD >2000V per MIL-STD-883, Method 3015; >200V using machine model (C = 200pF, R = 0)
- · Available in SSOP, QSOP, and TSSOP packages

APPLICATIONS:

• 3.3V High Speed Bus Switching and Bus Isolation

DESCRIPTION:

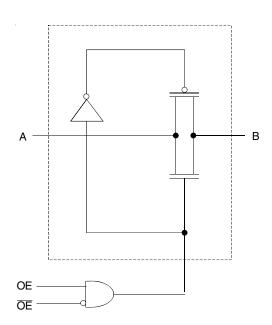
The CBTLV3862 provides ten bits of high-speed bus switching with low on-state resistance of the switch allowing connections to be made with minimal propagation delay.

The device is organized as one 10-bit bus switch. The switches are controlled by independent active-low enable (\overline{OE}) and active-high enable (\overline{OE}) controls.

 $\overline{\text{OE}}$ should be tied to VCC through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver, and OE should be tied to GND.

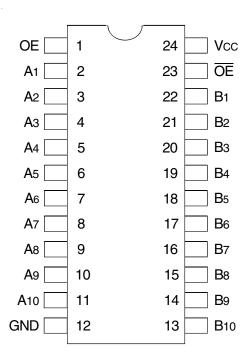
FUNCTIONAL BLOCK DIAGRAM

SIMPLIFIED SCHEMATIC, EACH SWITCH



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PIN CONFIGURATION



SSOP/ QSOP/ TSSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description Max		Unit
Vcc	Supply Voltage Range	-0.5 to 4.6	V
Vı	Input Voltage Range	-0.5 to 4.6	V
	Continuous Channel Current	128	mA
lık	Input Clamp Current, VI/O < 0	– 50	mA
Tstg	Storage Temperature Range	-65 to +150	°C

NOTE:

Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause
permanent damage to the device. This is a stress rating only and functional operation
of the device at these or any other conditions above those indicated in the operational
sections of this specification is not implied. Exposure to absolute maximum rating
conditions for extended periods may affect reliability.

FUNCTION TABLE(1)

Inputs			
OE	ŌĒ	Function	
L	L	Disconnect	
L	Н	Disconnect	
Н	L	A Port = B Port	
Н	Н	Disconnect	

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

OPERATING CHARACTERISTICS(1)

Symbol	Parameter	Test Conditions	Min.	Max.	Unit
Vcc	Supply Voltage	_	2.3	3.6	V
ViH	High-Level Control Input Voltage	Vcc = 2.3V to 2.7V	1.7		V
		Vcc = 2.7V to 3.6V	2	_	
VIL	Low-Level Control Input Voltage	Vcc = 2.3V to 2.7V	_	0.7	V
		Vcc = 2.7V to 3.6V	_	0.8	
TA	Operating Free-Air Temperature		-40	+85	°C

NOTE:

1. All unused control inputs of the device must be held at Vcc or GND to ensure proper device operation.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: TA = -40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit
Vık	Control Inputs, Data I/O	Vcc = 3V, II = -18mA		_	_	-1.2	V
lı	Control Inputs, Data I/O	Vcc = 3.6V, Vi = Vcc or GND)	_	_	±1	μA
loz	Data I/O	Vcc = 3.6V, Vo = 0V or 3.6V	switch disabled	_	_	5	μΑ
loff		Vcc = 0V, VI or Vo = 0V or 3	.6V	_	_	50	μA
Icc		Vcc = 3.6V, lo = 0, Vı = Vcc	or GND	_	_	10	μA
$\Delta lcc^{(2)}$	Control Inputs	Vcc = 3.6V, one input at 3V, other inputs at Vcc or GND		_	_	300	μΑ
Сі	Control Inputs	Vi = 3V or 0		_	4	_	pF
CIO(OFF)		Vo = 3V or 0 (switch off)		_	6	_	pF
	Vcc = 2.3V	Vı = 0	Io = 64mA	_	5	8	
	Typ. at Vcc = 2.5V		Io = 24mA	_	5	8	
Ron ⁽³⁾		Vı = 1.7V	Io = 15mA	_	27	40	Ω
		Vı = 0	Io = 64mA	_	5	7	
	Vcc = 3V		Io = 24mA	_	5	7	
		VI = 2.4V	Io = 15mA	_	10	15	

NOTES:

- 1. Typical Values are at Vcc = 3.3V, +25°C ambient.
- 2. The increase in supply current is attributable to each input that is at the specified voltage level rather than Vcc or GND.
- 3. This is measured by the voltage drop between the A and B terminals at the indicated current through the switch.

SWITCHING CHARACTERISTICS

		Vcc = 2.5V ± 0.2V		$Vcc = 3.3V \pm 0.3V$		
Symbol	Parameter	Min.	Max.	Min.	Max.	Unit
tpD ⁽¹⁾	Propagation Delay	_	0.15	_	0.25	ns
	A to B or B to A					
ten	Output Enable Time	1	4.5	1	4.2	ns
	OE to A or B					
tois	Output Disable Time	1	5	1	5	ns
	OE to A or B					
ten	Output Enable Time	1	4.5	1	4.2	ns
	OE to A or B					
tois	Output Disable Time	1	5	1	6	ns
	OE to A or B					

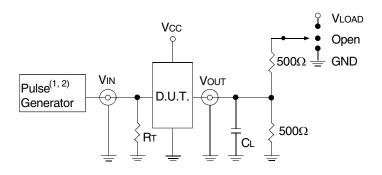
NOTE:

^{1.} The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance when driven by an ideal voltage source (zero output impededance).

TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	$Vcc^{(1)} = 3.3V \pm 0.3V$	Vcc ⁽²⁾ = 2.5V±0.2V	Unit
VLOAD	6	2 x Vcc	V
VIH	3	Vcc	V
VT	1.5	Vcc/2	V
VLZ	300	150	mV
VHZ	300	150	mV
CL	50	30	рF



Test Circuits for All Outputs

DEFINITIONS:

 $\ensuremath{\text{CL}}$ = Load capacitance: includes jig and probe capacitance.

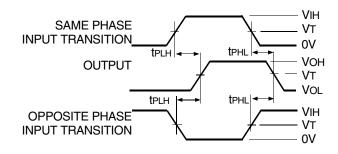
RT = Termination resistance: should be equal to ZOUT of the Pulse Generator.

NOTES:

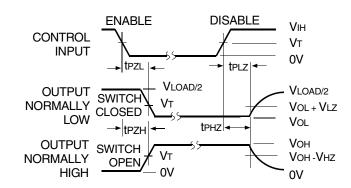
- 1. Pulse Generator for All Pulses: Rate \leq 10MHz; tF \leq 2.5ns; tR \leq 2.5ns.
- 2. Pulse Generator for All Pulses: Rate \leq 10MHz; tF \leq 2ns; tR \leq 2ns.

SWITCH POSITION

Test	Switch
tplz/tpzl	Vload
tphz/tpzh	GND
t _{PD}	Open



Propagation Delay

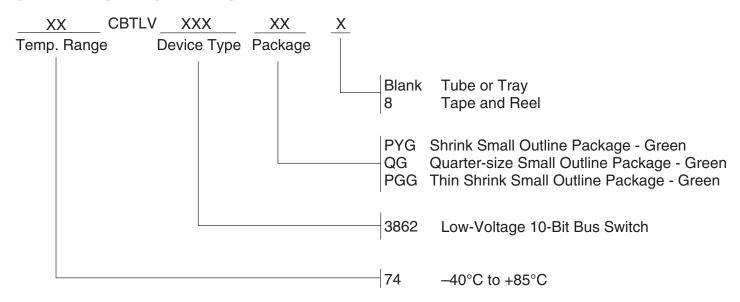


NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

Enable and Disable Times

ORDERING INFORMATION



Datasheet Document History

12/18/2014

Pg. 5

Updated the ordering information by removing the "IDT" notation, non RoHS part and by adding Tape and Reel information.



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