1 Scope

The present specifications shall apply to an RM11A.

2 Outline

Туре	Silicon Diode
Structure	Resin Molded
Applications	Commercial Frequency Rectification

3 Flammability

UL94V-0 (Equivalent)

4 Absolute maximum ratings

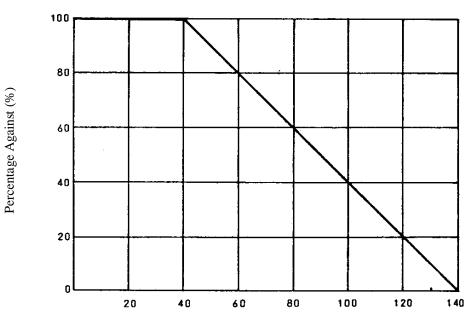
No.	Item	Symbol	Unit	Rating	Conditions
1	1 Transient Peak Reverse Voltage		V	600	
2	2 Peak Reverse Voltage		V	600	
3	Average Forward Current	I _{F(AV)}	А	1.2	Refer to derating curve in Section 6
4	Peak Surge Forward Current	I _{FSM}	А	100	10ms. Half sine wave, one shot
5	I ² t Limiting Value	I ² t	A ² s	50	1ms≦t≦10ms
6	Junction Temperature	T_j	°C	-40~+150	
7	Storage Temperature	T _{stg}	°C	-40~+150	

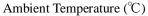
5 Electrical characteristics (Ta=25°C, unless otherwise specified)

No.	Item	Symbol	Unit	Rating	Conditions
1	Forward Voltage Drop	$V_{\rm F}$	V	0.92 max.	I _F =1.5A
2	Reverse Leakage Current	I _R	μΑ	10 max.	V _R =V _{RM}
3	Reverse Leakage Current Under High Temperature	H∙I _R	μΑ	50 max.	V _R =V _{RM} , T _j =100°C
4	Thermal Resistance	R _{th(j-l)}	°C/W	15 max.	Between Junction and Lead

6 Derating

Derating to the ambient temperature Power loss generated by voltage is not taken into consideration.





7 Package information 7-1Package type, physical dimensions and material 62. 5 ± 0.7 $7.2^{\pm 0.2}$ • Ж3 05 **φ4. 0** ±0. ² *φ*0. 78 ±0. X1 *1 The allowance position of Body against the center of whole lead wire is 0.5mm(max.) *2 The centric allowance of lead wire against center of physical body is 0.3mm(max.) Dimensions in mm *3 The burr may exit up to 2mm from the body of lead 7-2Appearance The body shall be clean and shall not bear any stain, rust or flaw. 7-3Marking ① Type number : RM11A 2 Lot number 1 First digit: Last digit of Year Second digit: Month From 1 to 9 for Jan. to Sep. O for Oct., N for Nov., and D for Dec. Cathode Mark 3 Lot number 2 (ten days) Top of the month •• Middle of month End of month . . . 1 1 2 3 2 3